Memory Product Specification

78.07G99.405
1024MB 128MX72-Bit DDR2 667 Fully Buffered DIMM

Description

The Module is 128M x 72-bit (1GB) DDR2 667 CL5 SDRAM , Dual ranks FBD Memory module . The Module consists of eighteen 64MX8 bit DDR2 667 Synchronous DRAMs in FBGA packages, Memory Module intented for mounting into 240-pin edge connector sockets. The electrical and mechanical specifications are as follows

Features

- **JEDEC Standard**
- **DDR2 Speed Grade : 667Mbps**
- **FBD DIMM : 240-pin**
- **Memory Organization : Dual ranks of x8 FBGA DRAM chip**
- **DDR2 DRAM interface : SSTL_18**
- **CAS latency : 5-5-5**
- **Bandwidth : 4300MB/s**
- **VDD voltage : 1.8+-0.1V**
- **VDDQ voltage : 1.8+-0.1V**
- **Serial presence detect with EEPROM.**
- **PCB height : 1.195 inch**
- **RoHS Compliant**